SMARC-sAMX6i







SMARC - Smart Mobility Architecture

- ► Ultra low power COM based on Freescale™ i.MX6 family
- ► SoC scalable from Solo Core up to Quad Core
- ► Commercial and industrial temperature versions



► TECHNICAL INFORMATION

PROCESSOR	NXP i.MX 6 Solo, Dual and Quad Core ARM Cortex-A9 800 MHz, 1.0 Ghz, 1.2 GHz	
GRAPHICS	Dual Display HD 1080p encode and decode 2D and 3D acceleration	
MEMORY	DDR3 up to 2 GByte, memory down	
FLASH	Up to 64 GByte NAND/eMMC on-module (Custom Option)	
ETHERNET	10/100/1000 Mbit/sec	
USB	3x USB 2.0 port (one OTG)	
DISPLAY	Parallel LCD 18/24 bit LVDS Single Channel 18/24bit HDMI	
IMAGE CAPTURE INTERFACES	2 Interfaces (PCAM, CSI)	
SERIAL	2x RX/TX (Ser1/3); 2x UART (Ser0/2)	
ADDITIONAL INTERFACES	Up to 3 PCIe x1, MLB150, 12x GPIOs, SDIO, SATA eMMC, $2x$ SPI, $5x$ I^2 C, $2x$ I^2 S, SPDIF, WDT, $2x$ CAN, Battery and System Management	
SW SUPPORT	Linux	
POWER	tbd	
THERMAL	Commercial temperature: 0 °C to +60 °C and Industrial temperature: -40 °C to 85 °C (800 MHz versions)	
POWER SUPPLY	3 V to 5.25 V - Operates directly from single level Lithium Ion cells, or fixed 3.3 V or 5 V power supplies	
COMPLIANCE	Form Factor: 82 x 50 mm Compliance: SMARC (Smart Mobility Architecture) specification by SGET	

▶ ORDERING INFORMATION

ARTICLE*	PART NO.	DESCRIPTION
SMARC-SAMX6I 0.8 512/4 GB	51003-0540-08-1	SMARC with NXP i.MX6 Solo, 800 MHz single core, 512 MByte DRAM memory down, 4 GByte onboard Flash, industrial temperature, no SATA support
SMARC-SAMX6I 2X0.8 1/4 GB	51003-1040-08-2	SMARC with NXP i.MX6 Dual, 800 MHz dual core, 1 GByte DRAM memory down, 4 GByte onboard Flash, industrial temperature, with SATA support
SMARC-SAMX6I 4X0.8 1/4 GB	51003-1040-08-4	SMARC with NXP i.MX6 Quad, 800 MHz quad core, 1 GByte DRAM memory down, 4 GByte onboard Flash, industrial temperature, with SATA support
SMARC-SAMX6I 4X0.8 2/2 GB	51003-2040-08-4	SMARC with NXP i.MX6 Quad, 800 MHz quad core, 2 GByte DRAM memory down, 4 GByte onboard Flash, industrial temperature, with SATA support

^{*}Please ask for more SoC or memory configurations.

► GLOBAL HEADQUARTERS

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