

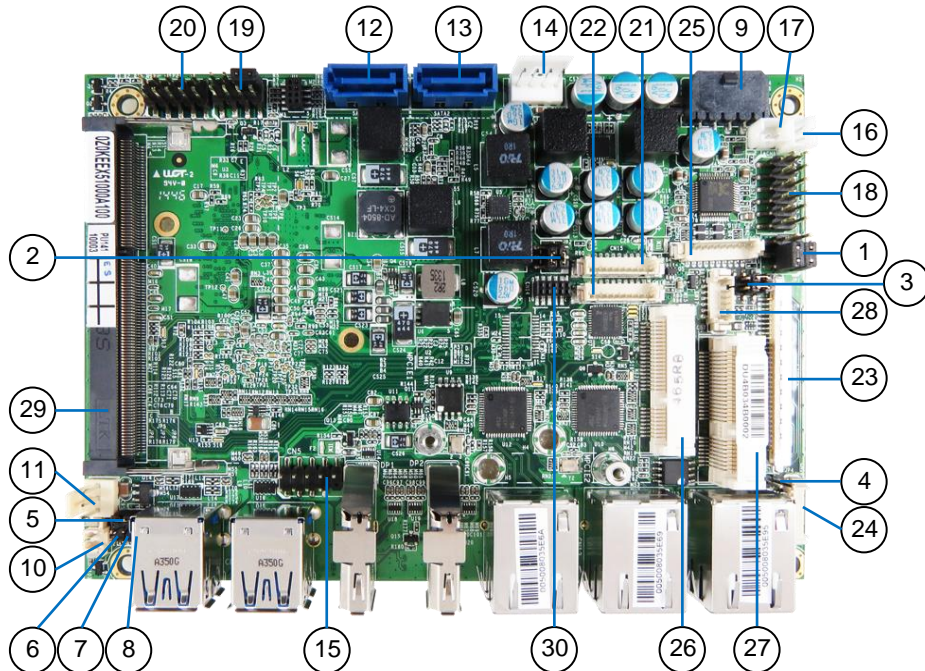
▶ ECX-BDW-U - QUICK INSTALLATION GUIDE

Thank you for purchasing the Kontron ECX-BDW-U embedded board. This document provides information to allow you to quickly install this product.

Packing Checklist

1. Take the ECX-BDW-U out of the packing box and check if the unit is properly secure in the plastic bag.
2. Check the contents of the carton box: (*: optional)
 - ▶ ECX-HSW-U main board
 - ▶ 1x DC IN Cable
 - ▶ COM Port Cable*
 - ▶ DIO Cable*
 - ▶ USB Cable*
 - ▶ SATA Cable With Power*
 - ▶ SATA Cable*
 - ▶ Audio Cable*
 - ▶ 3G Mini SIM Card Holder Kit*

Jumper and Internal Connector Locations



► Jumper List

Item	Designation	Description
1	JP1	Panel & Backlight Power Selection for LVDS1
2	JP2	AT_ATX Mode / MPCIE1 mSATA / mPCIe Selection
3	JP3	Backlight Power Enable Selection for LVDS1
4	JP4	MPCIe Activity LED Indication
5	JP5	USB Power Selection
6	JP6	Flash Description Security Over-ride
7	JP7	RTC Reset Selection
8	JP8	SRTC Reset Selection

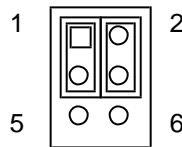
► Internal Connector List

Item	Designation	Description
9	CN4	Power Input Wafer
10	BAT1	CR2032 Battery Power Input Wafer
11	FAN1	CPU FAN Wafer
12	SATA1	Serial ATA Port 0 Connector
13	SATA2	Serial ATA Port 1 Connector
14	CN1	SATA HDD Power Output Wafer
15	CN5	USB 2.0 Port 10, 11 Pin Header
16	CN7	Right Channel 2W Audio AMP Output Wafer
17	CN8	Left Channel 2W Audio AMP Output Wafer

Item	Designation	Description
18	CN9	Audio Pin Header
19	FP1	Front Panel 1 Pin Header
20	FP2	Front Panel 2 Pin Header
21	CN15	RS232/422/485 Port 1 Wafer
22	CN16	RS232/422/485 Port 2 Wafer
23	LVDS1	Primary LVDS Panel Connector
24	CN18	Backlight Power Output Wafer for LVDS1
25	CN10	Digital Input / Output Wafer
26	MPCIE1	Mini-PCIE Express v1.2 Socket (Full Size)
27	MPCIE2	Mini-PCIE Express v1.2 Socket (Half Size)
28	CN17	SIM Interface Wafer for MPCIE2
29	DIMM1	DDR3 Memory SO-DIMM Socket
30	CN14	P80 Header

Jumper Settings

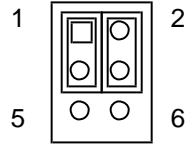
► Panel & Backlight Power Selection (JP1)



Jumper 1 Position		Description
Pin 1-3	Pin 3-5	
X	-	Backlight Power = +12V
-	X	Backlight Power = +5V
Jumper 2 Position		Description
Pin 2-4	Pin 4-6	
X	-	Panel Power = +3,3V
-	X	Panel Power = +5V

“X” = Jumper set (short) and “-” = jumper not set (open)

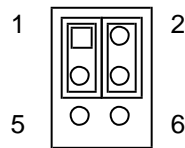
▶ AT_ATX Mode / MPCIE1 mSATA / mPCIE Selection (JP2)



Jumper 1 Position		Description
Pin 1-3	Pin 3-5	
X	-	ATX Mode (Default)
-	X	AT Mode
Jumper 2 Position		Description
Pin 2-4	Pin 4-6	
X	-	MPCIE1 mSATA Selected (Default)
-	X	MPCIE1 mPCIE Selected

"X" = Jumper set (short) and "-" = jumper not set (open)

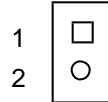
▶ Backlight Power Enable Selection (JP3)



Jumper 1 Position		Description
Pin 1-3	Pin 3-5	
X	-	Backlight Enable Voltage = +3.3V
-	X	Backlight Enable Voltage = +5V
Jumper 2 Position		Description
Pin 2-4	Pin 4-6	
X	-	Active High
-	X	Active Low

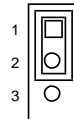
"X" = Jumper set (short) and "-" = jumper not set (open)

▶ MPCIE Activity LED Indication (JP4)



Pin	Signal
1	LED+
2	LED-

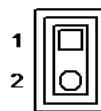
▶ USB Power Selection (JP5)



Jumper Position		Description
Pin 1-2	Pin 2-3	
X	-	USB power will be cut off in S4 & S5 state.
-	X	USB power will be cut off in Deep S5 state.

“X” = Jumper set (short) and “-” = jumper not set (open)

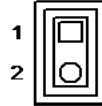
▶ Flash Description Security Over-ride (JP6)



Jumper Position		Description
Pin 1-2		
X		Enabled
-		Disabled

“X” = Jumper set (short) and “-” = jumper not set (open)

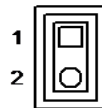
▶ RTC Reset Selection (JP7)



Jumper Position	Description
Pin 1-2	
X	Enable Clear CMOS RTC content (board does not boot with the jumper in this position)
-	Normal operation (default position)

“X” = Jumper set (short) and “-” = jumper not set (open)

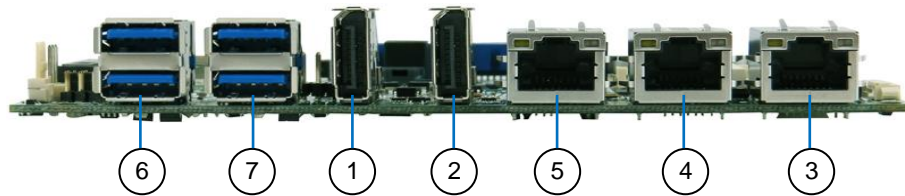
▶ SRTC Reset Selection (JP8)



Jumper Position	Description
Pin 1-2	
X	Clear ME Registers
-	Normal Operation

“X” = Jumper set (short) and “-” = jumper not set (open)

Real I/O Panel Connector Locations



Item	Designation	Description
1	DP1	DP Connector
2	DP2	DP Connector
3	CN11	GbE LAN1 RJ45 Connector
4	CN12	GbE LAN2 RJ45 Connector
5	CN13	GbE LAN3 RJ45 Connector
6	CN2	USB3.0 Port 0,1 Connector
7	CN3	USB3.0 Port 2,3 Connector